

Patent 237/117 21039-7096

# BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Wei Ming Hu et al.

Serial No.: 09/223,660

Filed: December 30, 1998

For: METHOD AND SYSTEM FOR DIAGNOSTIC PRESERVATION OF THE STATE OF A COMPUTER SYSTEM

Group Art Unit: 2189

Examiner: Tim T. Vo

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#### REPLY BRIEF UNDER 37 CFR § 1.193

### Mail Stop Appeal Brief - Patents

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

The applicants submit this Reply Brief pursuant to 37 CFR § 1.193 in response to the Examiner's Answer mailed on July 2, 2003.

#### **REMARKS**

The Examiner maintains, in the Examiner's Answer mailed on July 2, 2003, that claims 1-72 are unpatentable over U.S. Patent No. 5,485,573 to Tandon (hereinafter "Tandon") in view of U.S. Patent No. 6,195,760 to Chung et al. (hereinafter "Chung") and further in view of U.S. Patent No. 4,164,017 to Randell et al. (hereinafter "Randell").

Claim 1 is representative of the rejected claims. Claim 1 recites "preserving in place the state of a first set of system resources after the failure occurs in the computer system." (emphasis added). The Examiner states in the July 2, 2003 Examiner's Answer:

"preserving in place the state of a first set of system resources after the failure 3. occurs in the computer system." ((the breadth of this is limitation, a primary computer having resources such as network and I/O resources is running and when there is a failure, the previous state of the primary computer would then save into a particular location of memory (in place) after a failure). Appellant has never defined what the phrase "in place" means in the claims. Claims are given their broadest reasonable interpretation consistent with the specification. It is proper to use the specification to interpret what the applicant mean by a word or phrase recited in the claim. However, it is not proper to read limitations appearing in the specification into the claim when these limitations are not recited in the claim. Therefore, it would not be proper for the examiner to give words of the claim special meaning when no such special meaning has been defined by the applicant in the written description. Furthermore, it would not be proper for the examiner to allow a claim and issue the applicant with an examiner's statement of reasons for allowance setting forth the special definition given to the words of the claim when no such special definition has been defined by the applicant in the written description). Thus, the examiner's interpretation of the claim scope is consistent with the term used.

(July 2, 2003 Examiner's Answer, pgs. 6-7). This is the first time that the Examiner has asserted that he interprets the claim element to mean "a primary computer having resources such as network and I/O resources is running and when there is a failure, the previous state of the primary computer would then save into a particular location of memory (in place) after a failure."

"When examining claims for patentability, claims are interpreted as broadly as is reasonable and consistent with the specification." In re Thrift, 63 U.S.P.Q.2d (BNA) 2002, 2006 (Fed. Cir. 2002) (emphasis added). Saving "the previous state of the primary computer . . . into a particular location of memory," as argued by the Examiner, requires copying the state of the primary computer and storing that copy at a memory location. The specification of the subject application, however, specifically discusses this option in the background section as being inadequate, problematic, and costly. See pg. 1, 1. 16 to pg. 2, 1. 21. Further, the specification of the subject application expressly states that the present invention does not require making a copy of the state of a system and storing that copy in memory. See pg. 2, 1. 22 to pg. 3, 1. 5. Thus, the Examiner's interpretation of the claim is inconsistent with the specification.

In addition, the Examiner contends that he interprets the claim element to mean storing "the previous state of the primary computer." (emphasis added). The claim element, however, makes it clear that the state of the system resources that is preserved is the state the system resources were in after the failure occurred and is not the state the system resources were in before the failure occurred, as asserted by the Examiner. Therefore, the Examiner's contention of the claim scope is not consistent with the language used.

Moreover, saving "the previous state of the primary computer . . . into a particular location of memory," as argued by the Examiner, necessitates the use and movement of resources, which could change or modify the state of the resources that are suppose to be preserved. Claim 1, in contrast, recites "preserving in place the state of a first set of system resources after the failure occurs in the computer system." (emphasis added). Merriam-Webster defines "in place" as "in an original position . . . in the same spot without forward or backward movement." Merriam-Webster Online, http://www.m-w.com/. For example, freezing or suspending the first set of system resources to

insure that the state of those resources are not modified or changed. Hence, the Examiner's interpretation of the claim is unreasonable as it completely disregards certain terms in the claim.

#### The Examiner further states:

- 10. Tandon and Chung do not teach preserving in place the state of a first set of system resources after a failure occurs in the computer system. This means a first set of system resources as being a primary computer running database DBMS with computer resources such as network and I/O resources that would preserve its previous state in place after the failure. However, Randell teaches a computer system which performs actions in order to manipulate the states or values of items of information that is a program to be carried out on a computer system is constructed from identifiable operations for error recovery (see column 1 lines 5-50). These citations are equivalent to the present invention that is preserving in place the state of a first set of system resources after the failure occurs in the computer system. Therefore, it would have been obvious to include such teach preserving the state of a computer system after the failure occurs into the system of Tandon and Chung to diagnose and analyze the reason for failure and thereby preventing errors in future. (For more clarification, see below).
- 11. Randell teaches a computer program having provision for error recovery is constructed from a number of identifiable operation states after a failure occurs in the computer system. Randell teaches a program that forms a nested set of blocks and it is desired to restore the apparatus to its previous state in place by preserving the state at the beginning of each block. Further, column 1 lines 25-50, Randell describes each beginning of blocks in a program indicates a state of operation in the computer system and to determine whether there is a need to preserve that state of operation. For example, to preserve the previous state in place of operation after a failure, Randell teaches each of a plurality of items information at the beginning of each block wherein the item information change state during such block so that the condition prevailing at the beginning of the block can be restored by having a first memory means for storing the most recent state of each of the item information, second memory means for storing previous states of the item information (see column 1 lines 25-50). This process is saving previous state of the computer in place into a particular location of memory, which discussed above in paragraph number 3.

(July 2, 2003 Examiner's Answer, pgs. 9-10).

#### Randell discloses:

This invention relates to computer systems and similar devices which perform actions in order to manipulate the states or values of items of information.

A program to be carried out on a computer system is constructed from identifiable operations, each of which constitutes a block and consequently the complete program can be regarded as a set of blocks. Many blocks are themselves constructed from further smaller blocks so that the complete program forms a nested set of blocks. On completion of a block, it is frequently desired to restore the apparatus to state in which it was at the beginning of the block. One way of doing this is to make a separate record in the memory of the computer to the initial states of all items of information in the system. This is extremely wasteful of storage capacity since, in many blocks, only a comparatively small number of such items of information will change.

According to the invention, there is provided data processing apparatus for use with a central processor of a computer system or similar device in processes comprising a set of program blocks, said apparatus being operative to preserve the state taken up by each of a plurality of items of information at the beginning of each block where said items of information change state during such block so that the conditions prevailing at the beginning of the block can be restored, said apparatus comprising first memory means for storing the most recent state of each of said items of information, second memory means for storing previous states of said items of information, means responsive to said second memory means for transferring the existing state of an item of information from said first memory means to said second memory means when the state of said item of information changes and restoring to the first memory means the state of an item of information stored in the second memory means, and means for associating additional information with said items of information to control said transfer and restoration.

In one form of the invention, said means for associating additional information with the items of information comprises further memory means addressable with said first memory means for determining whether there is a need to preserve the state of each item of information in said first memory means if said item of information changes state during the current block.

(Col. 1, 1l. 5-51).

Randell, as asserted by the Examiner, only discloses storing the "previous" state of various items of information in the system. Claim 1, in contrast, recites "preserving in place the state of a first set of system resources <u>after the failure</u> occurs in the computer system." (emphasis added). Additionally, claim 1 does <u>not</u> recite "preserving the state of a computer system after the failure

occurs" as contended by the Examiner. Claim 1 recites "preserving in place the state of a first set of system resources after the failure occurs in the computer system." (emphasis added).

Further, as stated by the Examiner, "Randell teaches restor[ing] the apparatus to its previous state . . . at the beginning of each block" after failure occurs. Whereas, claim 1 recites "preserving in place the state of a first set of system resources after the failure occurs in the computer system." (emphasis added). Hence, Randell teaches away from the claim element since it discloses overwriting the most recent state of the items of information in the first memory means with the previous state of the items of information in the second memory means in order to restore the system to the state it was in prior to the error. See col. 1, 11. 39-43.

Therefore, even if Randell, Tandon, and Chung were combined, the combination neither teaches nor discloses "preserving in place the state of a first set of system resources after the failure occurs in the computer system" as recited in claim 1. Accordingly, applicants respectfully submit that claim 1 is patentable over Tandon in view of Chung and further in view of Randell.

### **CONCLUSION**

For the above reasons, applicants respectfully submit that rejection of claims 1-72 based on 35 U.S.C. § 103(a) has been overcome. Accordingly, applicants request that the Board of Patent Appeals and Interferences overrule the Examiner and allow claims 1-72.

Respectfully submitted,

Bingham McCutchen LLP

Dated: 8-27-03

By:

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| ·   | )<br>,                              |

## TRANSMITTAL OF REPLY BRIEF UNDER 37 CFR § 1.193

Mail Stop Appeal Brief - Patents Commissioner for Patents

P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Transmitted herewith for the above-identified application are the following:

- 1. Reply Brief Under 37 CFR § 1.193 (7 pages); and
- 2. Return Postcard.

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Respectfully submitted,

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Dated: 8-27-63

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